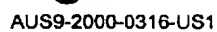


**SECRET**



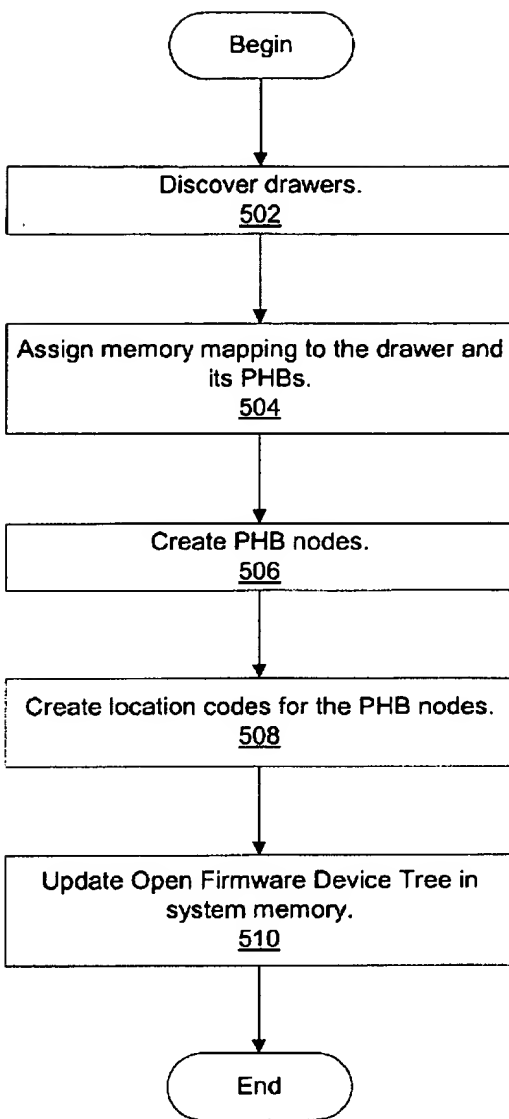
The diagram illustrates a Logically Partitioned Platform (200). At the top, four separate boxes represent operating systems: OS 202, OS 204, OS 206, and OS 208. Each OS box is connected by a vertical line to a central horizontal bar labeled Open Firmware 210. Below the Open Firmware bar is a box labeled Kernel 212. The entire system is contained within a large rectangular frame. Below the Kernel box, the platform is divided into Partitioned Hardware 230. This section contains several components arranged in a grid-like fashion: four Processor boxes (232, 234, 236, 238) in the top row; a Service Processor box (290) and a Storage box (270) in the middle row; an NVRAM box (298) in the middle row; four Memory boxes (240, 242, 244, 246) in the bottom row; and eight I/O Adapter boxes (248, 250, 252, 254, 256, 258, 260, 262) arranged in two columns on the right side of the hardware section.

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Figure 3 is a block diagram of a system architecture. The diagram shows a Service Processor 302 connected to an SPCN/SN Table 324, which is connected to NVRAM 322. The Service Processor 302 is also connected to an SPCN Bus 380. The SPCN Bus 380 is connected to three I/O Drawers: 304, 306, and 308. Each I/O Drawer contains two PHB components: 310 and 312 for 304, 314 and 316 for 306, and 318 and 320 for 308. The SPCN Bus 380 is also connected to a Firmware 326 block. The SPCN Bus 380 is connected to the OF Device Tree 342, which is connected to System Memory 340.

300  
System  
**Figure 3**  
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**Figure 5**

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